

FIG. 1A (PRIOR ART)

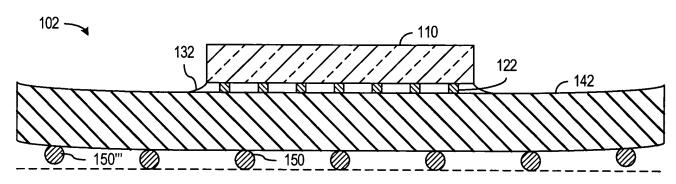


FIG. 1B (PRIOR ART)

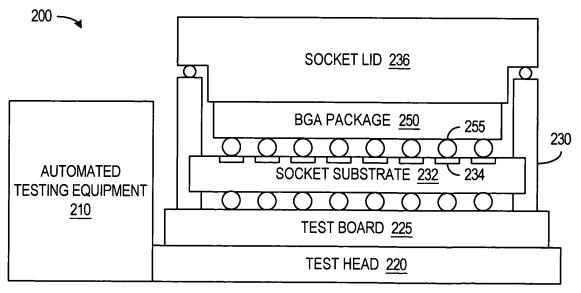
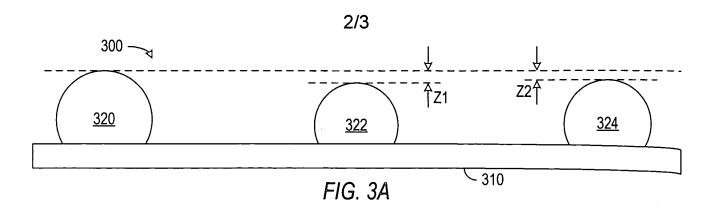
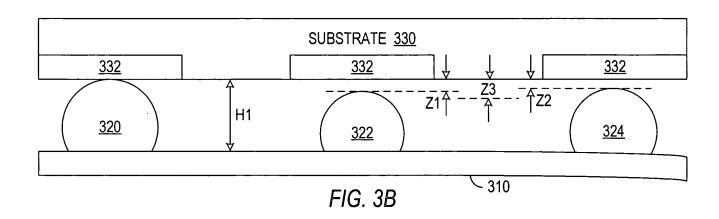
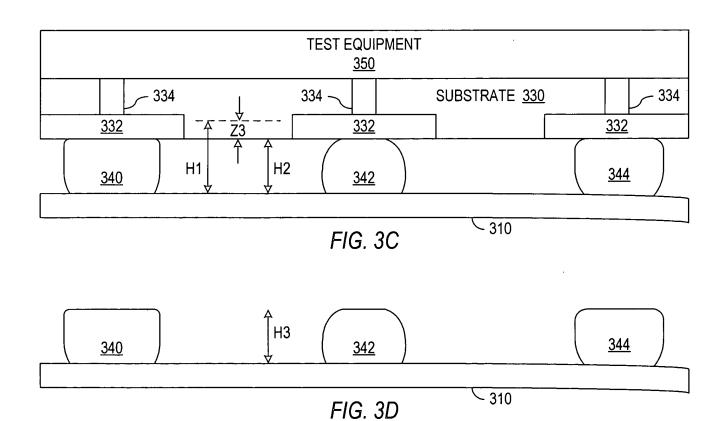


FIG. 2

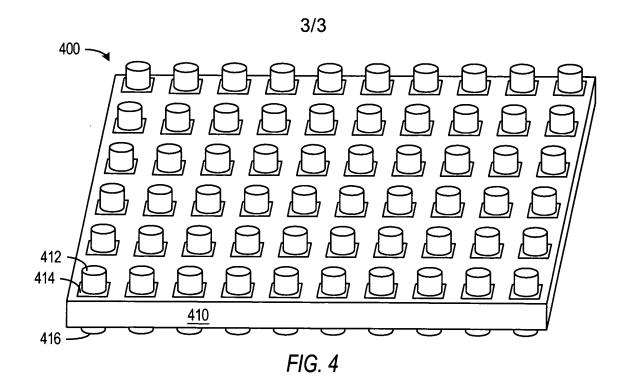
U.S. Pat. App. Entitled, "Planarizing and Testing of BGA Packages"; Inventors: DiOrio et al.; Atty. Docket No. MTB005US2P







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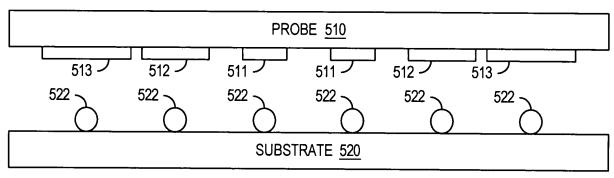


FIG. 5A

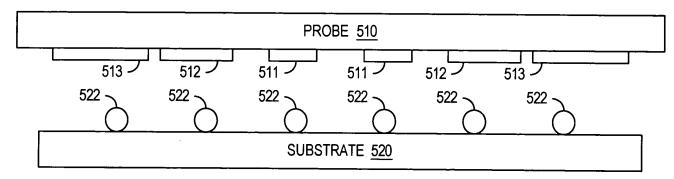


FIG. 5B